

All-Digital Frequency Synthesizer in Deep-Submicron CMOS

By Robert Bogdan Staszewski, Poras T. Balsara

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A new and innovative paradigm for RF frequency synthesis and wireless transmitter design

Learn the techniques for designing and implementing an all-digital RF frequency synthesizer. In contrast to traditional RF techniques, this innovative book sets forth digitally intensive design techniques that lead the way to the development of low-cost, low-power, and highly integrated circuits for RF functions in deep submicron CMOS processes. Furthermore, the authors demonstrate how the architecture enables readers to integrate an RF front-end with the digital back-end onto a single silicon die using standard ASIC design flow.

Taking a bottom-up approach that progressively builds skills and knowledge, the book begins with an introduction to basic concepts of frequency synthesis and then guides the reader through an all-digital RF frequency synthesizer design:

- Chapter 2 presents a digitally controlled oscillator (DCO), which is the foundation of a novel architecture, and introduces a time-domain model used for analysis and VHDL simulation
- Chapter 3 adds a hierarchical layer of arithmetic abstraction to the DCO that makes it easier to operate algorithmically
- Chapter 4 builds a phase correction mechanism around the DCO such that the system's frequency drift or wander performance matches that of the stable external frequency reference
- Chapter 5 presents an application of the all-digital RF synthesizer
- Chapter 6 describes the behavioral modeling and simulation methodology used in design

The final chapter presents the implementation of a full transmitter and experimental results. The novel ideas presented here have been implemented and proven in two high-volume, commercial single-chip radios developed at Texas Instruments: Bluetooth and GSM.

While the focus of the book is on RF frequency synthesizer design, the techniques can be applied to the design of other digitally assisted analog circuits

as well. This book is a must-read for students and engineers who want to learn a new paradigm for RF frequency synthesis and wireless transmitter design using digitally intensive design techniques.

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Editorial Review

From the Back Cover

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About the Author

ROBERT BOGDAN STASZEWSKI, PhD, is a Distinguished Member of Technical Staff with the Digital RF Processor Group of Texas Instruments, where he co-invented and developed the Digital RF Processor (DRPTM), a novel, all-digital transmitter and digitally intensive direct-sampling receiver architecture. Before joining Texas Instruments, Dr. Staszewski worked with Alcatel Network Systems as a design engineer.

PORAS T. BALSARA, PhD, is Professor of Electrical Engineering at the Erik Jonsson School of Engineering and Computer Science, The University of Texas at Dallas, where he teaches and conducts research in high-speed, low-power circuit design, VLSI circuits and architectures for signal processing and communication, and reconfigurable systems. He is also the Director of the University's Center for Integrated Circuits and Systems.

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