Computer Aided Logical Design with Emphasis on VLSI

By Frederick J. Hill, Gerald R. Peterson

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Tied to no particular set of computer-aided logic design tools, it advocates the new emphasis in VLSI design. Includes support of layout synthesis from description in a register transfer level language as well as from design capture. Contains a detailed introduction to Boolean algebra, Karnaugh maps and sequential circuits. In this edition discussion of combination logic has been extended; switching circuits updated; a comprehensive treatment of test generation for VLSI included.

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